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ITC 2800 MAIL ROOM

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT APPLICATION of

Jun KANAMORI

Group Art Unit: 2814

Serial No.: 09/398,189

Examiner: S. Rao

Filed: September 17, 1999

For: METHOD OF FABRICATING A SEMICONDUCTOR DEVICE WITH SELF-  
ALIGNED SILICIDE AREAS FORMED USING A SUPPLEMENTAL SILICON  
OVERLAYER

**REQUEST FOR RECONSIDERATION**

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

Date: May 9, 2001

Sir:

In response to the Final Office Action dated February 13, 2001, the following  
remarks are respectfully submitted in connection with the above-identified application.

**REMARKS**

Claims 1-9 and 19-22 are pending in the present application.

**Information Disclosure Statements**

An Information Disclosure Statement was filed along with the present application  
on September 17, 1999. However, the Examiner has failed to acknowledge receipt of  
the above noted Information Disclosure Statement filed on September 17, 1999. Th  
Examiner is r spectfully request d to acknowledge receipt of the Informati n